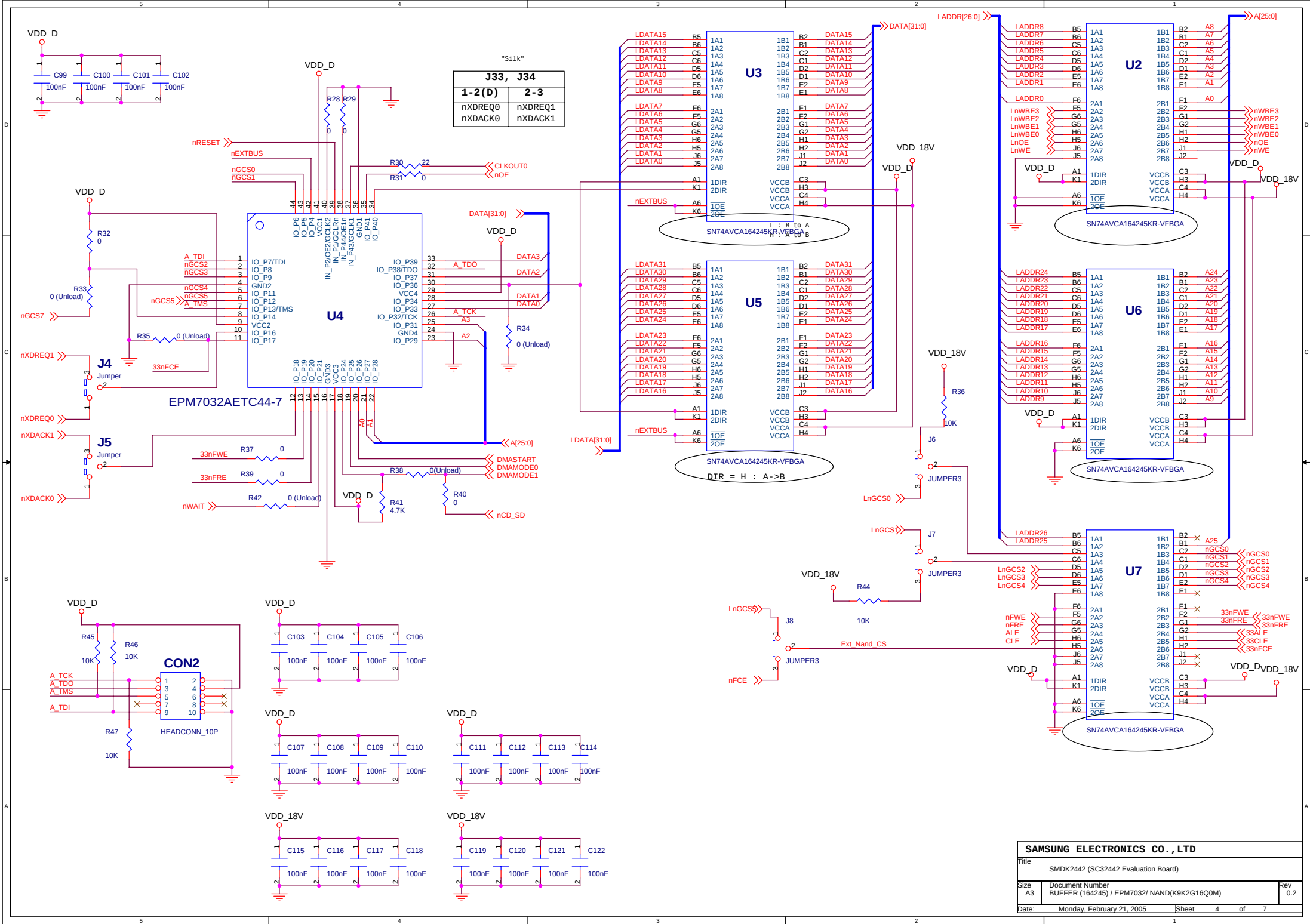
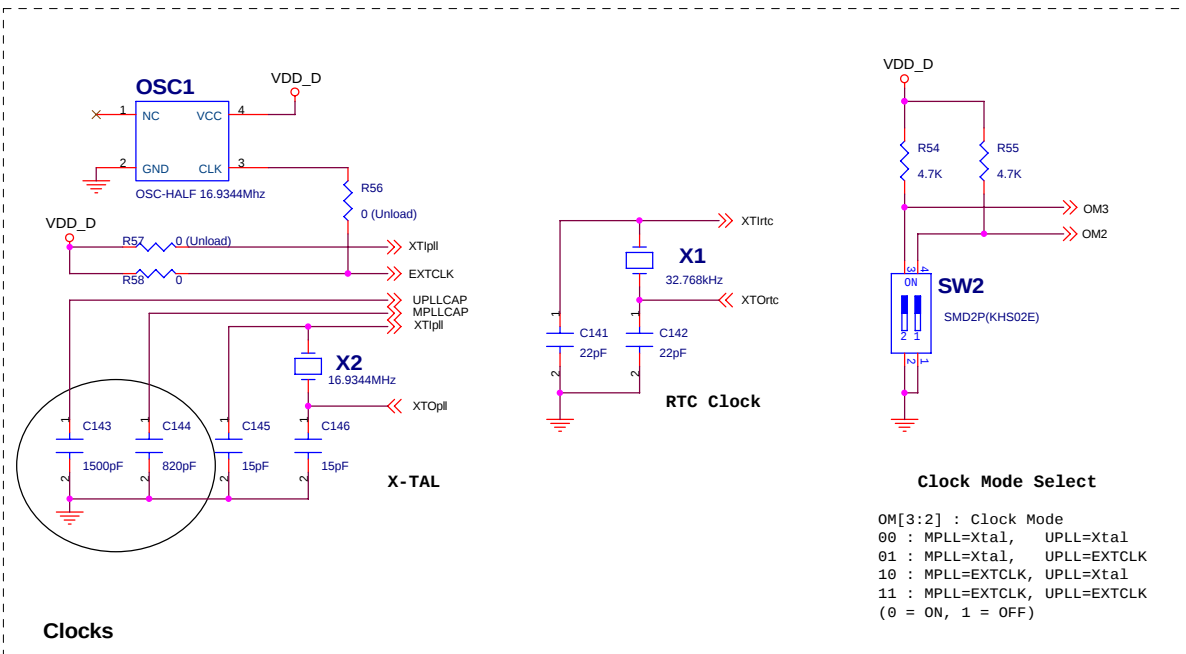
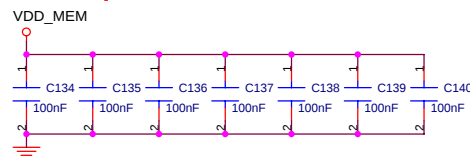
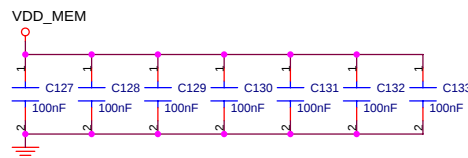
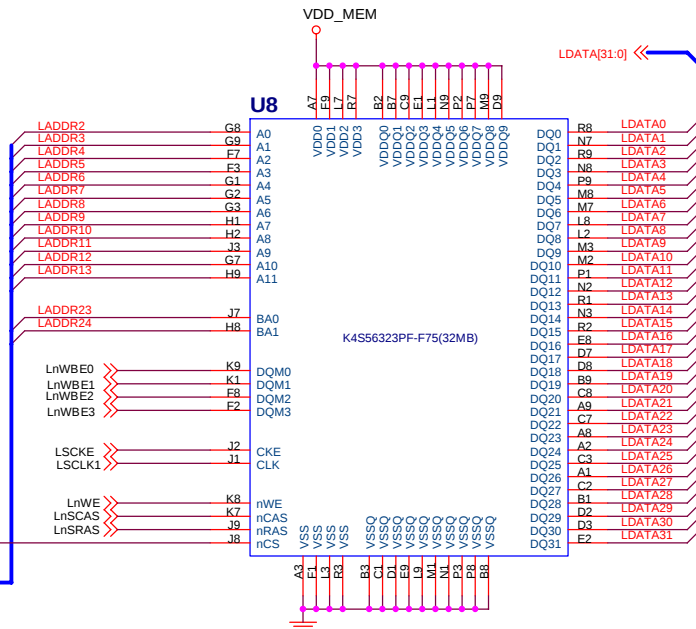
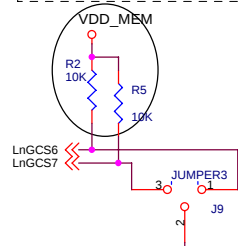
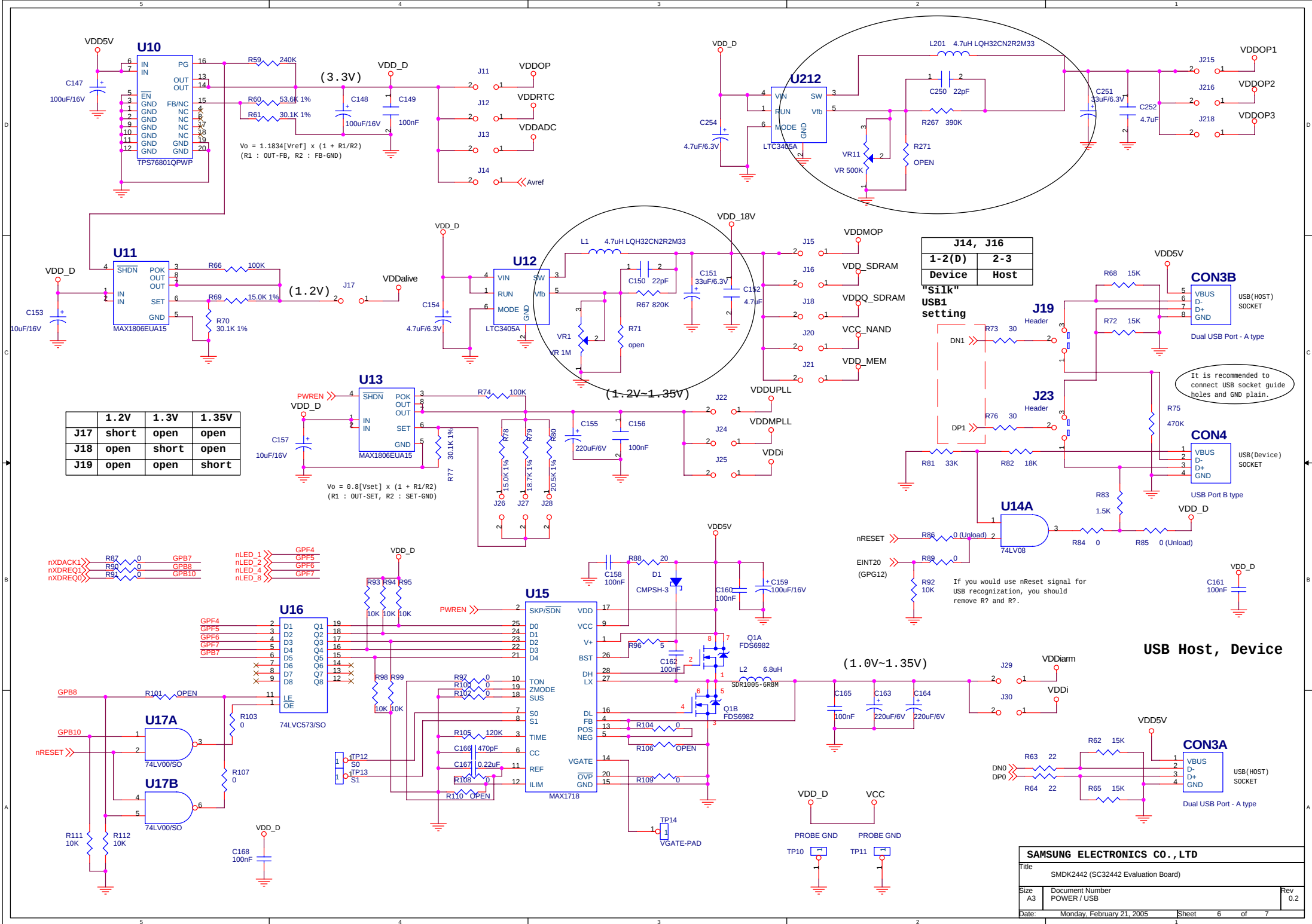


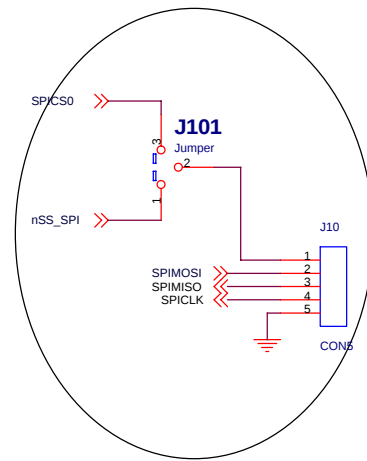
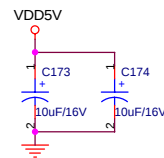
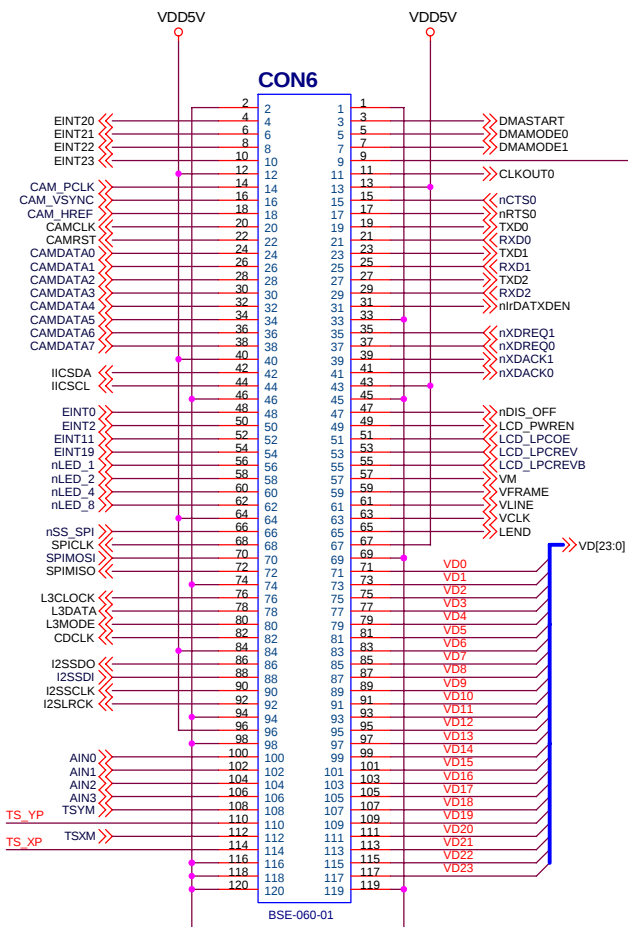
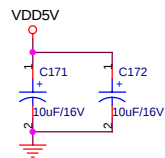
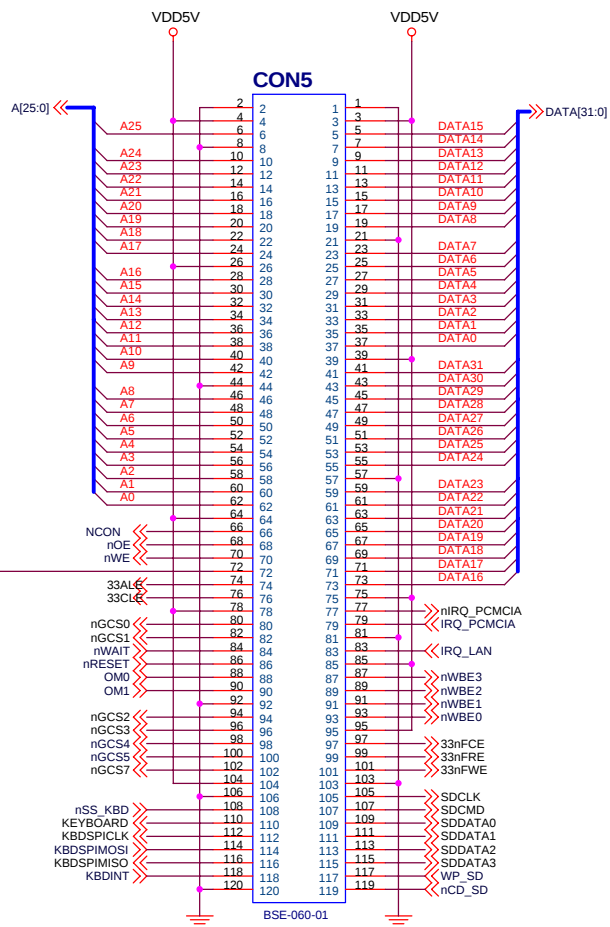


LnWE >> 1 TP4 nWE
 LSCKE >> 1 TP5 SCKE
 LSCLK0 >> 1 TP6 SCLK0
 LnGCS6 >> 1 TP7 NGCS6
 LnSRAS >> 1 TP8 nSRAS
 LnSCAS >> 1 TP9 nSCAS

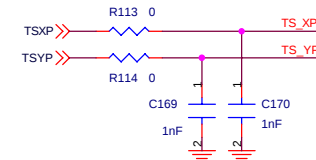
No route!!!
 Just only pads



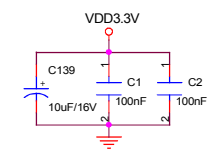
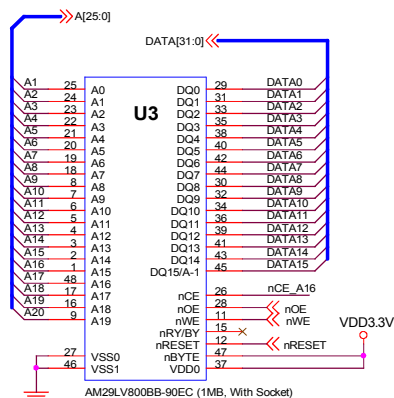




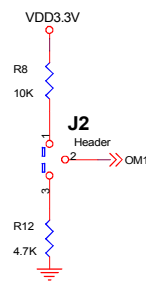
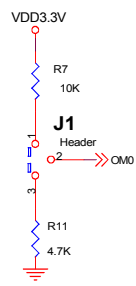
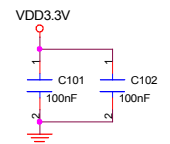
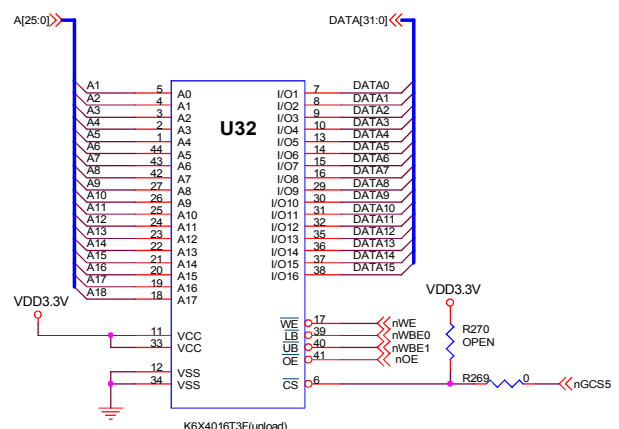
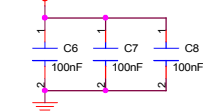
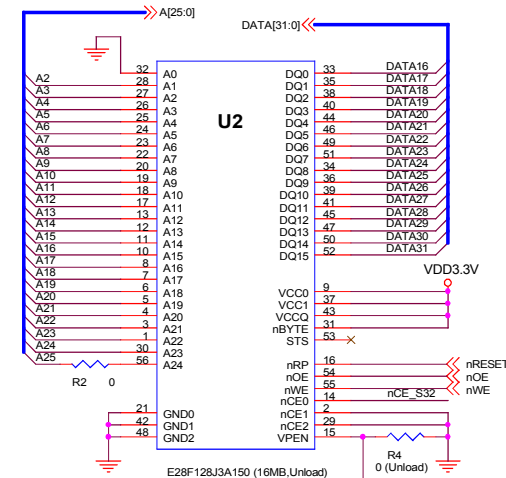
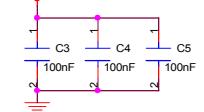
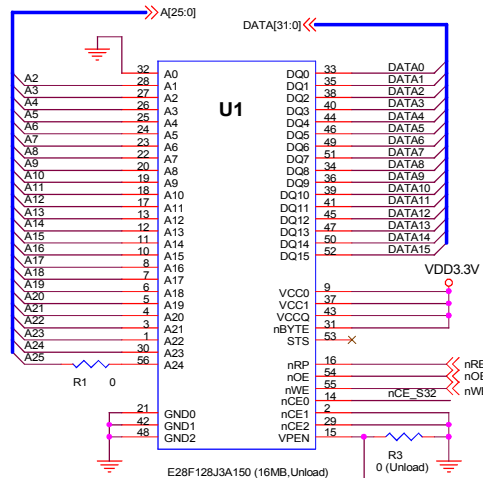
Place near the MCU



AMD Flash Memory (SOCKET)

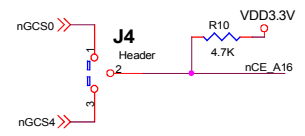
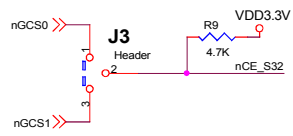


INTEL StrataFlash Memory (SMD Unload)

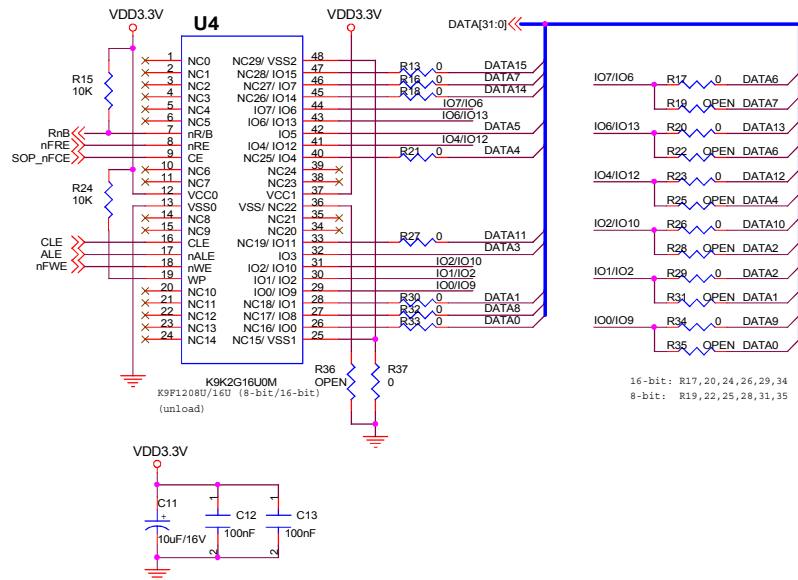


"Silk" Boot setting

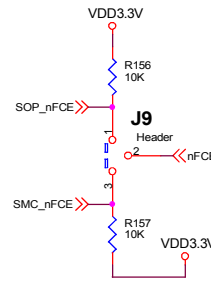
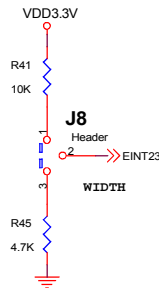
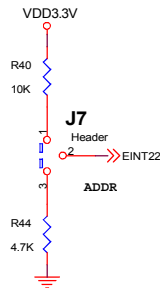
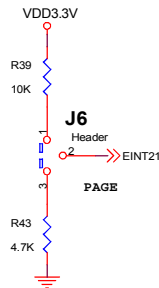
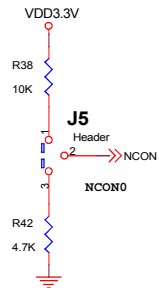
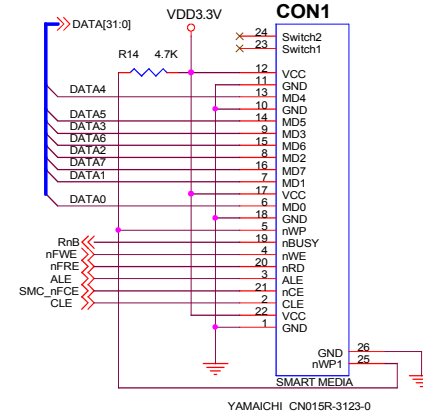
	AMD (D)	INTEL	NAND	Test mode
OM0 (J1)	1-2	2-3	2-3	1-2
OM1 (J2)	2-3	1-2	2-3	1-2
32bit (J3)	2-3	1-2	2-3	-
16bit (J4)	1-2	2-3	2-3	-



NAND Flash memory



SmartMedia Card (NAND Flash memory)



"Silk"

NAND setting

When OM[1:0]=2-3 (NAND boot)			
NCON0(J5)	Normal(2-3)(D)	Advanced(1-2)	
PAGE	2-3	256	1024
(J5)	1-2	512(D)	2048
ADDR	2-3	3 cycle	4 cycle
(J7)	1-2	4 cycle(D)	5 cycle
WIDTH	2-3	8-bit bus(D)	
(J8)	1-2	16-bit bus	
NAND	2-3	SMC enable(D)	
(J9)	1-2	SOP enable	

Setting Example

	SMC(K9S1208V0M)	K9K2G16U0M
J5	2-3	1-2
J6	1-2	2-3
J7	1-2	1-2
J8	2-3	1-2
J9	2-3	1-2

SAMSUNG ELECTRONICS CO.,LTD

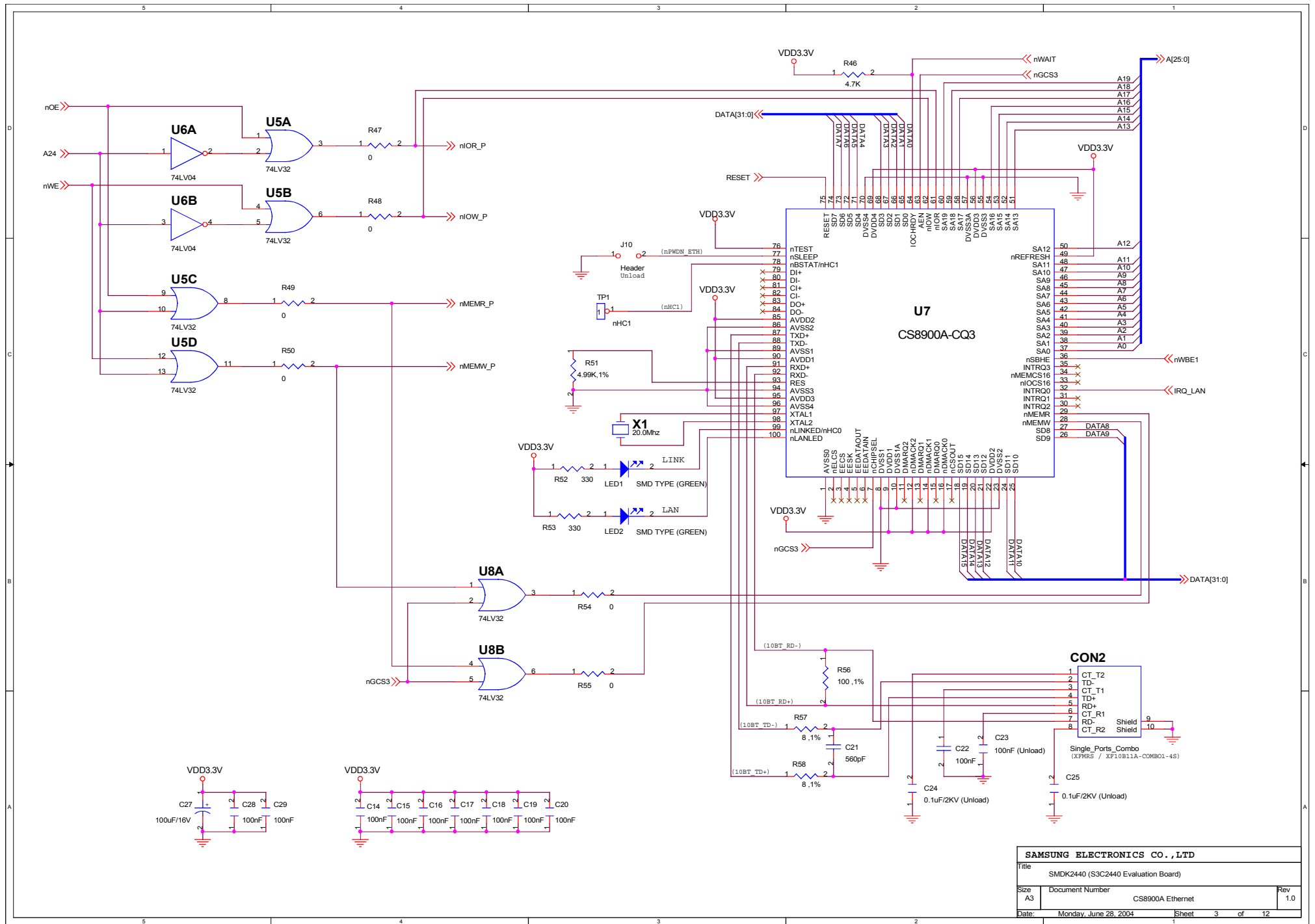
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SMDK2440 (S3C2440 Evaluation Board)

Size
A3 Document Number
NAND(SAMSUNG) FLASH MEMORY

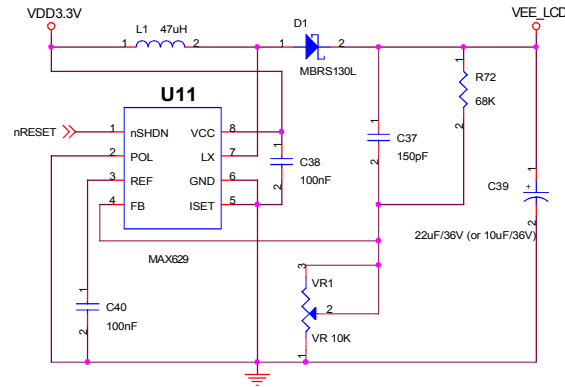
Rev
1.0

Date
Monday, June 28, 2004

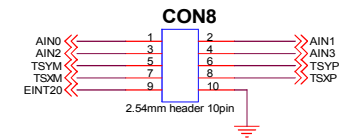
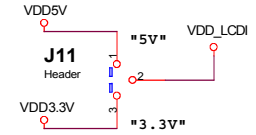
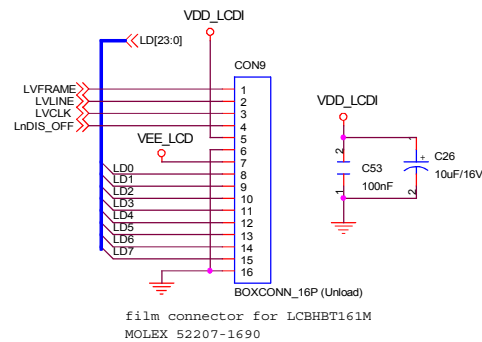
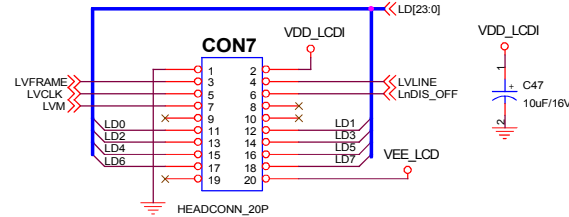
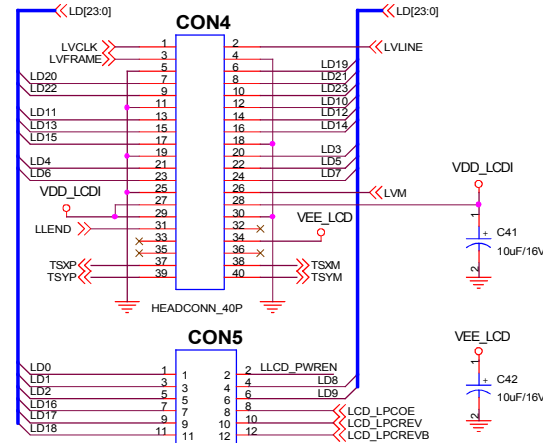
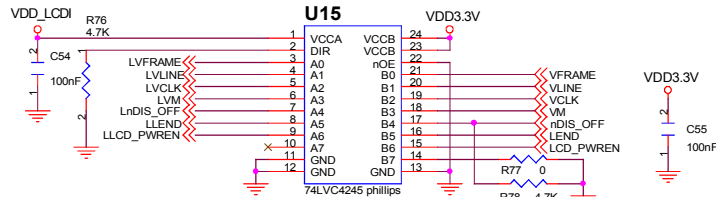
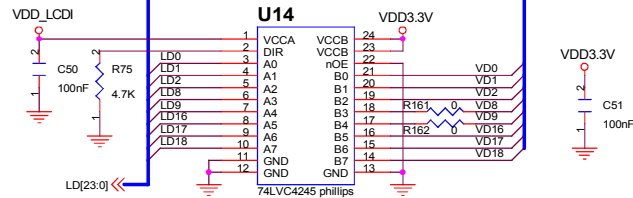
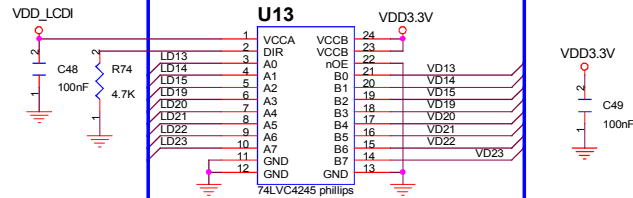
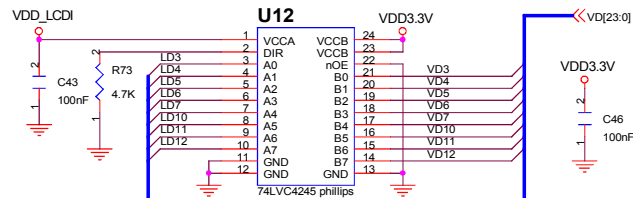
Sheet
2 of 12



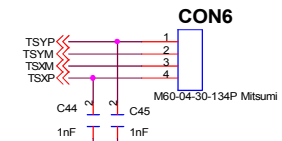
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Title	SMDK2440 (S3C2440 Evaluation Board)		
Size	Document Number	CS8900A Ethernet	
A3			Rev 1.0
Date:	Monday, June 28, 2004	Sheet	3 of 12



3.3V/24V DC/DC CONVERTER (STN LCD)



ADC
"1-2 Short Default"



Touch Screen

SAMSUNG ELECTRONICS CO.,LTD		
Title	SMDK2440 (S3C2440 Evaluation Board)	
Size	Document Number	Rev
A3	External LCD interface / Touch screen / ADC	1.0
Date:	Monday, June 28, 2004	Sheet 5 of 12

